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ADDITO		Alexandria, Virginia 22313-1450			
APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNUM		
09/699,772	10/30/2000	Sehat Sutardia	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
ERIC B. JANC	OFSKY	MP0018	6955		
ERIC B. JANOFSKY GENERAL PATENT COUNSEL MARVELL SEMICONDUCTOR, INC 700 FIRST AVENUE MAIL STOP 509 SUNNYVALE, CA 94089	MICONDUCTOR, INC		EXAMINER KINKEAD, ARNOLD M		
94089			ART UNIT	PAPER NUMBER	
			DATE MAILED: 07/25/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

	1	A	Application No.	Applicant(s)	
Office Action Summary		<u> </u>	09/699,772	SUTARDJA, SEHAT	-
	•		xaminer	Art Unit	
Paris de	The MAILING DATE of this communication Reply	A	rnold M Kinkead	2817	
	or Reply	uon appear	s on the cover sheet with the c	correspondence address	
after - If the	MAILING DATE OF THIS COMMUNICAT maions of time may be available under the provisions of 37 of SIX (6) MONTHS from the mailing date of this communicate period for reply specified above is less than thirty (30) day period for reply is specified above, the maximum statutory reto reply within the set or extended period for reply will, by reply received by the Office later than three months after the dipatent term adjustment. See 37 CFR 1.704(b).	REPLY IS TION. CFR 1.136(a). ation. ys, a reply withi	SET TO EXPIRE 3 MONTH(In no event, however, may a reply be tim In the statutory minimum of thick (20)	S) FROM	n.
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2a)⊠	Responsive to communication(s) filed or This action is FINAL .				
3)	Since this and the	This act	tion is non-final.		
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4)[💢 (Claim(s) 1-67 is/are pending in the appl		таую, 1000 С.Д. 11, 45	3 O.G. 213.	
4	a) Of the above claim(s)	lication.			
5)⊠ (a) Of the above claim(s) is/are with	hdrawn fro	m consideration.		ı
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/ · ·	1,3-0,10,14,16,56,61 and 62 islan	e rejected.			
8) 🗆 0	claim(s) <u>67</u> is/are objected to.				1
Application	laim(s) are subject to restriction an	nd/or electi	on requirement		
9)□ Th	e specification is objected to by the Every				
10)□ The	e drawing(s) filed on is/are: a) action to by the Exam	iiner.			
A	Applicant may not request that any objection to	ccepted or b))∐ objected to by the Examin	er.	
11)∐ The	Applicant may not request that any objection to proposed drawing correction filed on approved, corrected drawings are required in	ine drawin	g(s) be held in abeyance. See 3	7 CFR 1.85(a).	
lf	approved, corrected drawings are required:	io. a)_	ם disapproved בן disapproved	by the Examiner.	
12)[_] The	oath or declaration is objected to by the	Evamination	Office action.		
· ··ority unu	er 35 U.S.C. §§ 119 and 120				
13)☐ Acl	knowledgment is made of a claim for face				
a) <u></u> □ A	knowledgment is made of a claim for foreig	ign priority	under 35 U.S.C. § 119(a)-(d)	or (f).	
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2.[Certified copies of the priority documer	nts have be	en_received.		
3.	Certified copies of the priority document Copies of the certified copies of the priority document Copies of the Copies of th	nts have be	een received in Application No) .	
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itacnment(s)		··· Priority (under 35 U.S.C. §§ 120 and/o	or 121.	
Notice of Re Notice of Dra Information	ferences Cited (PTO-892) aftsperson's Patent Drawing Review (PTO-948) Disclosure Statement(s) (PTO-1449) Pager No(a)		4) Interview Summary (PTO-4 5) Notice of Informal Patent A	13) Paras N. ()	
Patent and Trademark ()-326 (Rev. 04-01		· · ·	6) [Other: (1/6 //	4 /2	
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DETAILED ACTION

Claim Objections

1. Claim 7 is objected to because of the following informalities: on line 10, "gain determining" should read—gain determining impedance—. Appropriate correction is required.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 61 and 62 are rejected under 35 U.S.C. 102(b) as being anticipated by Hajimiri et al(cited by applicant, of record).

The reference by Hajimiri et al discloses an oscillator (see figure 5 and p. 719,part B.) having cross-coupled, frequency-dependent feedback amplifier, where MOSFET(NMOS (lower pair)and PMOS) are implemented. As noted an attenuating device including tail capacitor as well as the LC tank capacitor is disclosed. Inductor(L) and capacitor(C) are shown. Finally, current sources(biasing) is represented by(Itail). (See p. 720, second col. last three lines where flicker noise is discussed-page 721). The sizing of the cross-coupled NMOS and PMOS mitigates/attenuates the 1/f noise that maybe upconverted to affect phase noise. The method steps being inherent.

Claim Rejections - 35 USC § 103

- The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth 3. in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- Claims 3-6,10,14,16, and 56 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hajimiri et 4. al(IEEE Journal of Solid State Circuits, May 99, cited by applicant) and further in view of Prior Art figure 2.

The reference by Hajimiri et al discloses an oscillator (see figure 5 and p. 719,part B.) having cross-coupled, frequency dependent feedback amplifier, where MOSFET(NMOS (lower pair)and PMOS) are implemented. As noted an attenuating device including tail capacitor as well as the LC tank capacitor is disclosed. Inductor(L) and capacitor(C) are shown. Finally, current sources(biasing) is represented by(Itail). (See p. 720, second col. last three lines where flicker noise is discussed-page 721). The sizing of the cross-coupled NMOS and PMOS mitigates/attenuates the 1/f noise that maybe upconverted to affect phase noise. The method steps being inherent.

The reference does not show several conventional differential amplifier configurations with regards the LC elements being arranged between the supply voltage terminals with first and second biasing sources coupled to the common source nodes of the NMOS transistors. Also not shown is an implementation of this LC oscillator within a RF communication transmitter for use as a Local oscillator for carrier signal generation.

With regards the latter idea, the use of LC oscillators for carrier signal generation is notoriously well known in the art and one of ordinary skill in the art would recognize the use of such low noise oscillators in RF applications.

Also, the Prior Art figure 2, is being relied upon for showing the general LC configuration where the frequency dependent gain impedances (L1,L2, C1,C2)are coupled between the supply voltage Vcc and ground. The use of a single current biasing means instead of two separate biasing means is a simple matter of design consideration and reduces the size of the overall circuit.

In light of the above one of ordinary skill in the art would have recognized that the particular LC oscillator configuration of Hajimiri et al with attenuating means would also be applicable to all other LC oscillator differential configurations(as shown by Prior art figure 2) to help reduce phase noise in RF communication application as is desired and notoriously well known in the art. The use of a single current means helping to reduce circuit complexity.

Allowable Subject Matter

Claims 2,7-9, 11-13,15, 17-55, 57-60, and 63-67 are allowed. The examiner could not find fair suggestion 5. in the prior art for the characterization of the attenuating device as in claim 2 (and 15)now allowed. The allowed independent claims 7,10, 11,12,15,25,35,46,57,62,63,64,65, and 66 claim additional components including second attenuating means and gain determining impedance/circuits that are not suggested in the prior art.. The use of programmable inductance/resistance is not shown for the current sources as claimed.

Response to Arguments

Applicant's arguments filed 04-10-03 have been fully considered but they are not persuasive. The examiner 6. has considered applicant's concern that the reference to Hajimiri et al does not teach attenuation of the 1/f noise,

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however, the reference does indeed talk about such noise and describes the effort with regards to making sure the cross-coupled NMOS/PMOS transistors are sized correctly to form part of the attenuating means that reduces the 1/f noise that may be upconverted and thus contribute to phase noise. The attenuating device includes the tail capacitor as well as it must be sized carefully too.

Conclusion

7. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Arnold M Kinkead whose telephone-number is 703-305-3486. The examiner can normally be reached on Mon-Fri, 8:30 am -5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 703-308-4909. The fax phone numbers for the organization where this application or

Application/Control Number: 09/699,772 Art Unit: 2817 Page 6 proceeding is assigned are 703-308-7724 for regular communications and 703-308-7724 for After Final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956. Arnold M Kinkead Primary Examiner Art Unit 2817 Arnold Kinkead July 17, 2003